

L Number	Hits	Search Text	DB	Time stamp
-	5330	CMOS same differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:02
-	427	(CMOS same differential) same impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:02
-	59	((CMOS same differential) same impedance) same match\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:07
-	30647	current near2 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:07
-	4	((((CMOS same differential) same impedance) same match\$3) same (current near2 mode)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:34
-	18	("4503340" "4616148" "5132576" "5309047" "5390147" "5438287" "5446396" "5612630" "5687123" "5691658" "5789981" "5793230" "5804994" "5808496" "5841718" "5850365" "6008673" "6081140").PN.	USPAT	2003/09/23 14:24
-	1860	push with pull with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:34
-	171	(push with pull with differential) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:34
-	92	((push with pull with differential) and CMOS) and impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 14:51
-	2	("5760601").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 16:38
-	2	("5898326" "5955889").PN.	USPAT	2003/09/23 16:37
-	3	driver with output with CMOS with differential with current adj1 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 16:47
-	4	("5666354" "5778204" "5900752" "6184714").PN.	USPAT	2003/09/23 16:39

-	80	driver with output with CMOS with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 16:47
-	5	(driver with output with CMOS with differential) and current adj2 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/23 16:47
-	2	("6075384").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 10:17
-	19	single near2 end\$3 with driver with CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 10:31
-	249	differential with CMOS with driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 10:31
-	20	(differential with CMOS with driver) same match\$3 with impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:14
-	14	("4180786" "4616148" "4843264" "4887047" "5165046" "5329190" "5357211" "5486782" "5552728" "5754480" "5821777" "5900779" "5963061" "6114895").PN.	USPAT	2003/09/30 10:34
-	7	("5164663" "5469081" "5523703" "5548226" "5686872" "5731711" "5898326").PN.	USPAT	2003/09/30 10:50
-	2	("5811984").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:20
-	962	current with bias with impedance with input	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:20
-	948	current with bias with impedance with output	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:20
-	408	(current with bias with impedance with input) and (current with bias with impedance with output)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:38
-	0	((current with bias with impedance with input) and (current with bias with impedance with output)) and CMOS with differential with driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:21

-	7	((current with bias with impedance with input) and (current with bias with impedance with output)) and CMOS with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:39
-	2	("5264744").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:30
-	85	(current with bias with impedance with input) with match\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:39
-	0	((current with bias with impedance with input) with match\$3) and CMOS with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 11:39
-	7	((current with bias with impedance with input) with match\$3) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 12:58
-	34	differential with CMOS with push with pull	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:01
-	2	(differential with CMOS with push with pull) same driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:08
-	2084	differential with input with driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:08
-	96	(differential with input with driver) same CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:22
-	2	("4719369").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:32
-	17	ground adj1 bounce same impedance with match\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 13:32
-	7	(ground adj1 bounce same impedance with match\$3) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 14:37
-	290	open with drain with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 14:38

-	1	(open with drain with differential) and CMOS with driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 14:38
-	8	(open with drain with differential) and impedance with match\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 14:51
-	6	(("4859880") or ("6437610") or ("5050194")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 14:56
-	2	("6198507").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 15:24
-	3	"6198507"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 15:25
-	4	"6288744"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 15:27
-	2	("4821105").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:00
-	2	("5767699").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:22
-	7	dual adj1 ended same impedance with (control\$3 or match\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:25
-	1	dual adj1 ended and CMOS and impedance with (control\$3 or match\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:25
-	2288	differential and CMOS and impedance with (control\$3 or match\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:25
-	2003	differential with impedance with (control\$3 or match\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:26
-	907	(differential with impedance with (control\$3 or match\$3)) same (output or transmit\$3) same (input or receiv\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:27

-	162	((differential with impedance with (control\$3 or match\$3)) same (output or transmit\$3) same (input or receiv\$3)) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:31
-	3	("2001004308").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:32
-	3	"2001004308"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:32
-	0	2001/0043080	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:37
-	2	"20010043080"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/09/30 16:37
-	2	6,519,000	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 08:25
-	636970	current near2 source with current mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 08:25
-	7188	current near2 source with current adj1 mirror	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 08:25
-	1941	(current near2 source with current adj1 mirror) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 08:25
-	116	(current near2 source with current adj1 mirror) with CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 08:26
-	35	((current near2 source with current adj1 mirror) with CMOS) with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 09:02
-	122	input with impedance with differential with bias\$2 with current	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 09:11
-	0	(input with impedance with differential with bias\$2 with current) same driver same CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 09:03

-	7	(input with impedance with differential with bias\$2 with current) same driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 09:03
-	317	impedance near2 (match\$3 or control\$3 or adjust\$3) with bias\$2 with current	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:51
-	29	(impedance near2 (match\$3 or control\$3 or adjust\$3) with bias\$2 with current) same differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 09:12
-	6	("4841175" "4999519" "5124580" "5146112" "5149992" "5541532").PN.	USPAT	2003/10/01 09:43
-	35	current near2 (mirror or source) with adjust\$3 with input near3 impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 10:29
-	26	current near2 (mirror or source) with adjust\$3 with input adj1 impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 10:33
-	2	(current near2 (mirror or source) with adjust\$3 with input near3 impedance) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 10:29
-	12	current near2 bias\$2 with adjust\$3 with input adj1 impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 10:39
-	23	current near2 bias\$2 with match\$3 with input adj1 impedance	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:27
-	2	("6452736").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:35
-	90	CMOS with (BJT or bipolar) with (similiar or equivalent)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:38
-	0	(CMOS with (BJT or bipolar) with (similiar or equivalent)) with current adj1 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:38
-	0	(CMOS with (BJT or bipolar) with (similiar or equivalent)) with differential	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:37

-	2	(CMOS with (BJT or bipolar) with (similiar or equivalent)) with driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:40
-	0	(CMOS with (BJT or bipolar) with (similiar or equivalent)) same current adj1 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:38
-	1	(CMOS with (BJT or bipolar) with (similiar or equivalent)) and current adj1 mode	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:38
-	7	(CMOS with (BJT or bipolar) with (similiar or equivalent)) same driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 11:41
-	17	(impedance near2 (match\$3 or control\$3 or adjust\$3) with bias\$2 with current) same driver	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 12:25
-	2	("4518880" "5774009").PN.	USPAT	2003/10/01 11:53
-	2	("6452736").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/01 13:44